

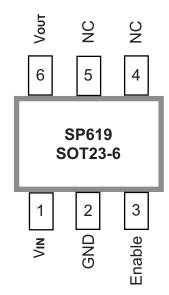
# **High Current Power Switch**

## **FEATURES**

- Max Rds(on) .377 Ohms
- Overcurrent deactivation 800mA
- Max leakage current less than 2µA while deactivated
- Small 6 pin SOT-23 package
- Built-in Over-temperature Protection
- 4.5V to 5.5V input voltage range

### **APPLICATIONS**

- Ultra low cost handsets
- PDA, DSC, MP3 players
- Cell phones
- Power Distribution Switch
- Battery-Charger Circuit



### . DESCRIPTION

The SP619 is a low RDS(ON) high current switch designed with precision current limiting to protect connected devices from damage due to a short circuit condition or against current surges that may cause the supply voltages to fall out of regulation. This switch is functional over an input voltage range of 2.5V to 7V, but is targeted at 5V applications. The SP619 is also protected from thermal overload which limits power dissipation. In shutdown mode, the supply current drops to 2µA.

# TYPICAL APPLICATION CIRCUIT SP619 Vout Cin .1µF EN ON/OFF 80K GND

### **ABSOLUTE MAXIMUM RATINGS**

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

| VIN0.3                               | √ to 7V |
|--------------------------------------|---------|
| Storage Temperature65°C to           | 150°C   |
| Junction Temperature                 | 150°C   |
| Lead Temperature (Soldering, 10 sec) | 300°C   |
| ESD RATING                           |         |
| Human Body Model                     | .2000V  |
| Machine Model                        | 200V    |
| OPERATING RATINGS                    |         |
| Thermal resistance SOT23-6           |         |
| Junction to Ambient                  | 191°C   |
| Junction to lead                     | 50°C    |

### RECOMMENDED OPERATING CONDITIONS

Unless otherwise specified:  $V_{IN} = 4.5V - 5.5V$ ,  $C_{IN} = 0.1 \mu F$ ,  $T_A = -10 ^{\circ} C$  to  $+ 85 ^{\circ} C$ 

| PARAMETER                                | CONDITIONS  | MIN  | TYP (Note 1) | MAX  | UNITS |
|--|---|------|--------------|------|-------|
| Operating Input Voltage<br>Range         |   | 4.5  |              | 5.5  | V     |
| Overcurrent Deactivation Range           | EN=1.5V   | 620  | 800          | 1010 | mA    |
| Overcurrent Duration Before Deactivation | EN=1.5V   | 1    |              | 5.25 | ms    |
| Shutdown Supply Current                  | V <sub>IN</sub> =4.5V   |      |              | 2    | μA    |
| Quiescent Supply Current                 | EN=1.5V V <sub>IN</sub> =4.5V<br>Iout=0mA                             |      |              | 350  | μА    |
| Quiescent Supply Current                 | EN=1.5V V <sub>IN</sub> =4.5V<br>IOUT=725mA                           |      |              | 5.25 | mA    |
| RDS(ON)                                  | V <sub>IN</sub> =4.5V I <sub>OUT</sub> =100mA<br>EN=1.5V              |      |              | .377 | Ω     |
| Post Fault Output Load for Recovery      | Enable=1.5V   | 10   | 15           | 23   | ΚΩ    |
| Post Fault Activation Turn<br>On Time    | Enable=1.5V   | 1    |              | 55   | ms    |
| Thermal Shutdown Die Temperature         | SP619 will self recover when temperature drops below the trip point . | 120  | 150          | 180  | °C    |
| Thermal Restart Die Temperature          |   | 90   | 120          | 135  | °C    |
| ENABLE Logic LOW                         | Driver is disabled  |      |              | .5   | V     |
| ENABLE Pin Logic HIGH                    | Driver is active  | .655 |              | 1.4  | V     |
| Turn On Delay                            | R <sub>L</sub> =100kΩ, C <sub>L</sub> = 0.01μF<br>(Note 2,3)          |      |              | 600  | μs    |
| Turn Off Delay                           | R <sub>L</sub> =100kΩ, C <sub>L</sub> = 0.01μF<br>(Note 2,4)          |      |              | 200  | μs    |
| Rise Time                                | R <sub>L</sub> =100kΩ, C <sub>L</sub> = 0.01μF<br>(Note 2)            |      |              | 100  | μs    |
| Fall Time                                | R <sub>L</sub> =100kΩ, C <sub>L</sub> = 0.01μF<br>(Note 2)            |      |              | 2500 | μs    |

Note 1: Typicals are T<sub>J</sub>=25°C

Note 2: Characterized, not 100% tested

Note 3: Turn on delay is measured from the time the enable pin is turned on to the time it takes the output to rise to 10% of its final value.

Note 4: Turn off delay is measured from the time the enable pin is turned to the time it takes for the output to fall to 90% of its current value.

### FAULT AND RECOVERY TRUTH TABLE\*

| Enable | Load     | Temperature<br>(TJ) | Previous<br>State | Switch | Current State/Fault<br>(Note*)                    |
|--------|----------|---------------------|-------------------|--------|---|
| Low    | Х        | X                   | X                 | Open   | Off   |
| High   | < 800 mA | < 163°C             | Off               | Closed | Normal  |
| High   | < 800 mA | > 163°C             | Off               | Open   | Thermal Cutoff                                    |
| High   | > 800 mA | < 163°C             | Off               | Open   | Overcurrent                                       |
| High   | > 800 mA | > 163°C             | Off               | Open   | Thermal Cutoff,<br>Overcurrent                    |
| High   | < 15kΩ   | < 163°C             | Overcurrent       | Open   | Load Condition                                    |
| High   | < 15kΩ   | > 163°C             | Overcurrent       | Open   | Thermal Cutoff,<br>Overcurrent, Load<br>Condition |
| High   | > 15kΩ   | < 163°C             | Overcurrent       | Closed | Normal  |
| High   | > 15kΩ   | > 163°C             | Overcurrent       | Open   | Thermal Cutoff                                    |
| High   | < 15kΩ   | < 120°C             | Thermal Cutoff    | Open   | Load Condition                                    |
| High   | < 15kΩ   | > 120°C             | Thermal Cutoff    | Open   | Load Condition,<br>Thermal Cutoff                 |
| High   | > 15kΩ   | < 120°C             | Thermal Cutoff    | Closed | Normal  |
| High   | > 15kΩ   | > 120°C             | Thermal Cutoff    | Open   | Thermal Cutoff                                    |

<sup>\*</sup>Note: This table is for fault conditions, not for continuous operation, else damage to the device may occur. In order to recover to the normal state after a Thermal Cutoff fault, the part's junction temperature must decrease below  $120^{\circ}$ C and the load on its output must be greater than  $15k\Omega$ .

### . PIN ASSIGNMENTS

| PIN<br>NUMBER | PIN<br>NAME | FUNCTION                              |
|---------------|-------------|---------------------------------------|
| 1             | Vin         | Input power supply pin (4.5V to 5.5V) |
| 2             | GND         | Ground connection                     |
| 3             | Enable      | A logic high turns on the switch      |
| 4             | NC          | No connect                            |
| 5             | NC          | No connect                            |
| 6             | Vouт        | Switch output                         |

The SP619 is a switch capable of handling up to 800mA of current.

The SP619 is targeted as a for 5V USB protection power distribution switch. And can be used in general power distribution applications where short circuits are likely to be encountered.

## **Short circuit operation**

When the SP619 enters a short circuit condition, the switch is disabled. The output of the SP619 will not restart until the output impedance is greater than  $15 \mathrm{K}\Omega$ . The enable pin can be used to re-enable the SP619 into any load condition that is not a fault condition. Refer to the truth table on page 4 for more information on the different SP619 switch states. The typical deactivation time is about 2ms.

### **Enable**

The enable pin allows easy control of the SP619. The enable pin should not be enabled high prior to a voltage being present on the input of the device. The enable pin should not exceed the input voltage by more than 0.1V due to an internal ESD diode. Doing so will affect the operation of the SP619 and could damage the device. In a typical application an  $80K\Omega$  resistor to GND should be used on the enable pin. This resistor will pull the enable low when an enable signal is not present. This prevents the SP619 from falsely turning on. The enable pin can also be used to restart the part into a load condition that is high in current. Please refer to the truth table on page 4 for more details.

### **Inrush Current**

The SP619 is a simple resistive switch. When the switch is turned on into a highly capacitive load there could be a significant

inrush current that can be encountered. At 6 volts in, the inrush current was about 250mA into a 100µF capacitor.

# **Output Voltage Rise Time**

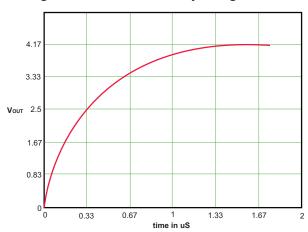
The output voltage of the SP619 has an output capacitance dependency on the slope of Vout. A simple RC circuit is created when the switch is turned on. Equation 1

$$V(t)=V_0+V_{IN}\left(\frac{-t}{1-e^{R_{DS(ON)} \cdot C_{OUT}}}\right)$$

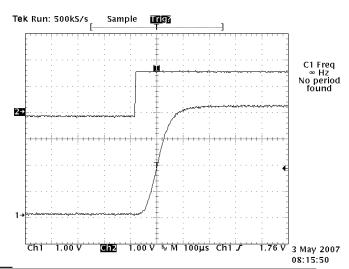
Where Vo is initial Voltage condition typically 0V

VIN is the input voltage RDS(ON) is the switch resistance COUT is the output capacitance

For 4.2V VIN and 100µF output capacitance we get about 150ns delay in figure 2.



This is comparable to actual measured value in figure 3.

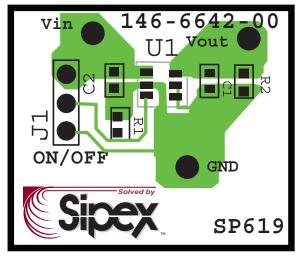


# **Overtemperature Protection**

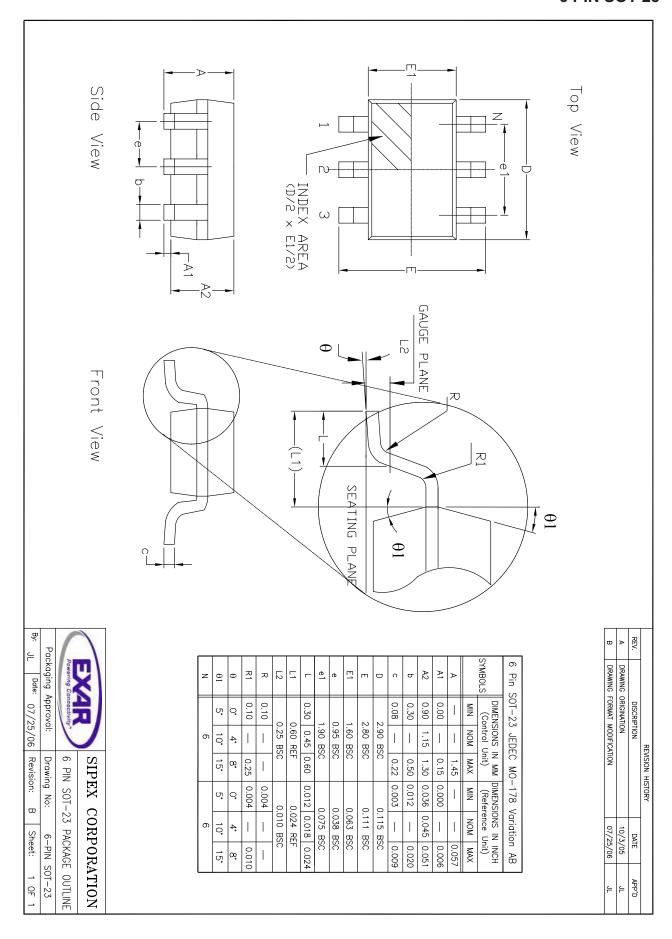
The SP619 has built-in overtemperature protection to protect the part against damage if the die temperature gets too hot. The typical thermal cutoff is about 150°C. The part will self recover if the temperature drops below the thermal restart threshold of about 120°C.

# **Layout Considerations**

The input and output decoupling capacitors should be placed as close as possible to the input and output pins. The GND pin should be stitched to the GND plain to help with thermal performance. The input and output traces should be as large as possible. The  $100 \text{K}\Omega$  is also recommended in the design and should be placed close to the output decoupling cap.



Recommended Layout



### ORDERING INFORMATION

| Part number  | Temperature range | Package Type          |
|--------------|-------------------|-----------------------|
| SP619EK-L    | From -10 to +85°C | Lead Free 6 pin SOT23 |
| SP619EK-L/TR | From -10 to +85°C | Lead Free 6 pin SOT23 |

/TR = Tape and Reel.

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