CREE 🔶 C3D12065A Silicon Carbide Schottky Diode **Z-Rec®** Rectifier

Features

- 650 Volt Schottky Rectifier .
- . Zero Reverse Recovery Current
- Zero Forward Recovery Voltage •
- High-Frequency Operation •
- Temperature-Independent Switching Behavior •
- **Extremely Fast Switching**
- Positive Temperature Coefficient on V_r

Benefits

- **Replace Bipolar with Unipolar Rectifiers**
- Essentially No Switching Losses •
- **Higher Efficiency** •
- **Reduction of Heat Sink Requirements** •
- Parallel Devices Without Thermal Runaway •

Applications

- Switch Mode Power Supplies (SMPS)
- . Boost diodes in PFC or DC/DC stages
- Free Wheeling Diodes in Inverter stages
- AC/DC converters

Maximum Ratings (T_c=25°C unless otherwise specified)

Symbol	Parameter	Value		Test Conditions	Note	
V _{RRM}	Repetitive Peak Reverse Voltage	650	v			
V _{RSM}	Surge Peak Reverse Voltage	650	v			
V _{DC}	DC Blocking Voltage	650	v		1	
I _F	Continuous Forward Current	35 16 12	A	T _c =25°C T _c =135°C T _c =150°C		
I _{FRM}	Repetitive Peak Forward Surge Current	51.5 33.5	A	$T_c=25^{\circ}$ C, t _p =10 ms, Half Sine Pulse $T_c=110^{\circ}$ C, t _p =10 ms, Half Sine Pulse		
I _{fsm}	Non-Repetitive Peak Forward Surge Current	104 82	А	$T_c=25$ °C, $t_p=10$ ms, Half Sine Pulse $T_c=110$ °C, $t_p=10$ ms, Half Sine Pulse		
I _{F,Max}	Non-Repetitive Peak Forward Current	1075 900	A	T_c =25°C, t_p =10 μ s, Pulse T_c =110°C, t_p =10 μ s, Pulse	Fig. 8	
P _{tot}	Power Dissipation	143 62	w	T _c =25°C T _c =110°C	Fig. 4	
dV/dt	Diode dV/dt ruggedness	200	V/ns	V _R =0-650V		
∫i²dt	i²t value (Per Leg)	54 33.5	A ² s	T _c =25°C, t _p =10 ms T _c =110°C, t _p =10 ms		
T _J , T _{stg}	Operating Junction and Storage Temperature	-55 to +175	°C			
	TO-220 Mounting Torque	1 8.8	Nm Ibf-in	M3 Screw 6-32 Screw		

Package



V_{RRM}

 \mathbf{Q}_{c}

 $I_{r}(T_{c}=135^{\circ}C)$

650 V

16 A

34 nC

=

=

=

TO-220-2



Part Number	Package	Marking
C3D12065A	TO-220-2	C3D12065



Electrical Characteristics

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.5 2.0	1.8 2.4	V	I _F = 12 A T _J =25°C I _F = 12 A T _J =175°C	Fig. 1
I _R	Reverse Current	15 29	74 295	μA	V _R = 650 V T _J =25°C V _R = 650 V T _J =175°C	Fig. 2
Q _c	Total Capacitive Charge	34		nC	V _R = 400 V, I _F = 12 A di/dt = 500 A/μs T _J = 25°C	Fig. 5
С	Total Capacitance	641.5 57 47.5		pF	V _R = 0 V, T _J = 25°C, f = 1 MHz V _R = 200 V, T _J = 25°C, f = 1 MHz V _R = 400 V, T _J = 25°C, f = 1 MHz	Fig. 6
E _c	Capacitance Stored Energy	4.8		μJ	V _R = 400 V	Fig. 7

Note: This is a majority carrier diode, so there is no reverse recovery charge.

Thermal Characteristics

Symbol	Parameter	Тур.	Unit	Note
R _{ejc}	Thermal Resistance from Junction to Case	1.05	°C/W	Fig. 9

Typical Performance



Figure 1. Forward Characteristics





Typical Performance



Figure 3. Current Derating



Figure 5. Total Capacitance Charge vs. Reverse Voltage











Typical Performance



Figure 7. Capacitance Stored Energy

Figure 8. Non-repetitive peak forward surge current versus pulse duration (sinusoidal waveform)

10E-3



Figure 9. Transient Thermal Impedance



Package Dimensions







O CASE



POS	Inc	hes	Millin	Millimeters		
P03	Min	Max	Min	Мах		
А	.381	.410	9.677	10.414		
В	.235	.255	5.969	6.477		
С	.100	.120	2.540	3.048		
D	.223	.337	5.664	8.560		
D1	.457	490	11.60-1	11.60-12.45 typ		
D2	.2773	303 typ	7.04-7	.70 typ		
D3	.2442	252 typ	6.22-	6.4 typ		
E	.590	.615	14.986	15.621		
E1	.302	.326	7.68	8.28		
E2	.227	251	5.77	6.37		
F	.143	.153	3.632	3.886		
G	1.105	1.147	28.067	29.134		
Н	.500	.550	12.700	13.970		
L	.025	.036	.635	.914		
М	.045	.055	1.143	1.550		
Ν	.195	.205	4.953	5.207		
Р	.165	.185	4.191	4.699		
Q	.048	.054	1.219	1.372		
S	3°	6°	3°	6°		
Т	3°	6°	3°	6°		
U	3°	6°	3°	6°		
V	.094	.110	2.388	2.794		
W	.014	.025	.356	.635		
х	3°	5.5°	3°	5.5°		
Y	.385	.410	9.779	10.414		
z	.130	.150	3.302	3.810		

View A-A

1. Dimension L, M, W apply for Solder Dip Finish

Recommended Solder Pad Layout

PIN 2 O



TO-220-2

Part Number	Package	Marking
C3D12065A	TO-220-2	C3D12065



Note: Recommended soldering profiles can be found in the applications note here: http://www.wolfspeed.com/power_app_notes/soldering







Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Notes

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfpseed representative or from the Product Ecology section of our website at http://www.wolfspeed.com/Power/Tools-and-Support/Product-Ecology.

REACh Compliance

REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Cree representative to insure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body
nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited
to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical
equipment, aircraft navigation or communication or control systems, or air traffic control systems.

Related Links

- Cree SiC Schottky diode portfolio: http://www.wolfspeed.com/Power/Products#SiCSchottkyDiodes
- Schottky diode Spice models: http://www.wolfspeed.com/power/tools-and-support/DIODE-model-request2
- SiC MOSFET and diode reference designs: http://go.pardot.com/l/101562/2015-07-31/349i

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