

Vishay Semiconductors

IR Receiver Modules for Remote Control Systems



FEATURES

- Very low supply current
- Photo detectors and preamplifier in one package
- Internal filter for PCM frequency
- Supply voltage: 2.5 V to 5.5 V
- · Improved immunity against ambient light
- Insensitive to supply voltage ripple and noise
- Material categorization:
 for definitions of compliance please see

for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN

FREE GREEN (5-2008)

DESCRIPTION

The TSOP852..AP5, TSOP854..AP5 series are miniaturized receiver modules for infrared remote control systems. A PIN diode and a preamplifier are assembled on a PCB, the epoxy lens cap is designed as an IR filter.

The demodulated output signal can be directly decoded by a microprocessor. The TSOP852..AP5 is compatible with all common IR remote control data formats. The TSOP854..AP5 is optimized to suppress almost all spurious pulses from energy saving fluorescent lamps but will also suppress some data signals.

This component has not been qualified according to automotive specifications.

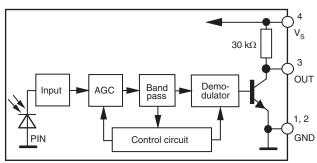
PARTS T	ABLE			
AGC		LEGACY, FOR LONG BURST REMOTE CONTROLS (AGC2)	RECOMMENDED FOR LONG BURST CODES (AGC4) (1)	
	36 kHz	TSOP85236AP5	TSOP85436AP5 (2)(3)(4)	
Carrier frequency	38 kHz	TSOP85238AP5	TSOP85438AP5 (5)(6)(9)	
	40 kHz	TSOP85240AP5	TSOP85440AP5	
	56 kHz	TSOP85256AP5	TSOP85456AP5 (7)(8)	
Package	Pinning	1, 2 = GND, 3 = OUT, 4 = V _S		
Package	Dimensions (mm)	4.5 H x 5.0 W x 1.3 L		
Mounting		SMD		
Application		Remote control		
Best remote control code		⁽²⁾ RC-5 ⁽³⁾ RC-6 ⁽⁴⁾ Panasonic ⁽⁵⁾ NEC ⁽⁶⁾ Sharp ⁽⁷⁾ r-step ⁽⁸⁾ Thomson RCA ⁽⁹⁾ r-map		

Note

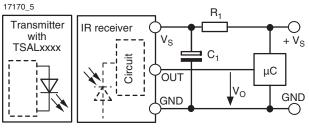
20445-3

(1) We advise try AGC4 first if the burst length is unknown

BLOCK DIAGRAM



APPLICATION CIRCUIT



 $\rm R_1$ and $\rm C_1$ are recommended for protection against EOS. Components should be in the range of 33 Ω < $\rm R_1$ < 1 k Ω , C, > 0.1 μF .

Rev. 1.8, 27-Feb-15 **1** Document Number: 85197



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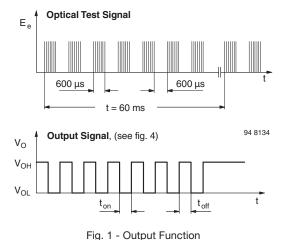
ABSOLUTE MAXIMUM R	BSOLUTE MAXIMUM RATINGS				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Supply voltage (pin 4)		Vs	-0.3 to +6	V	
Supply current (pin 4)		I _S	3	mA	
Output voltage (pin 3)		Vo	-0.3 to (V _S + 0.3)	V	
Output current (pin 3)		lo	5	mA	
Junction temperature		Tj	100	°C	
Storage temperature range		T _{stg}	-25 to +85	°C	
Operating temperature range		T _{amb}	-25 to +85	°C	
Power consumption	T _{amb} ≤ 85 °C	P _{tot}	10	mW	

Note

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

LECTRICAL AND OPT	CAL AND OPTICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply voltage		V _S	2.5		5.5	V
Supply current (pin 4)	$V_S = 3.3 \text{ V}, E_V = 0$	I _{SD}	0.27	0.35	0.45	mA
	$E_v = 40 \text{ klx, sunlight}$	I _{SH}		0.45		mA
Transmission distance	$E_v = 0$, IR diode TSAL6200, $I_F = 250$ mA, test signal see fig. 1	d		35		m
Output voltage low (pin 3)	I _{OSL} = 0.5 mA, E _e = 0.7 mW/m ² , test signal see fig. 1	V _{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: t_{pi} - 5/ f_o < t_{po} < t_{pi} + 6/ f_o , test signal see fig. 1	E _{e min.}		0.15	0.35	mW/m²
Maximum irradiance	t _{pi} - 5/f _o < t _{po} < t _{pi} + 6/f _o , test signal see fig. 1	E _{e max.}	30			W/m ²
Directivity	Angle of half transmission distance	Ψ1/2		± 75		deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)



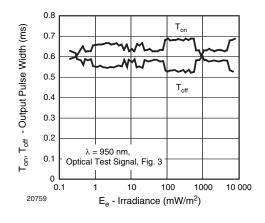


Fig. 2 - Output Pulse Diagram



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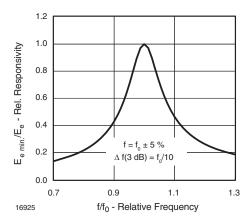


Fig. 3 - Frequency Dependance of Responsivity

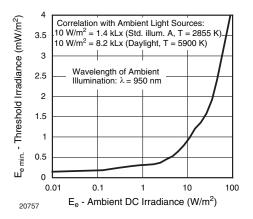


Fig. 4 - Sensitivity in Bright Ambient

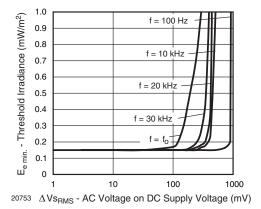


Fig. 5 - Sensitivity vs. Supply Voltage Disturbances

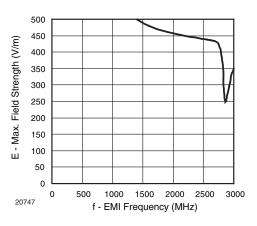


Fig. 6 - Sensitivity vs. Electric Field Disturbances

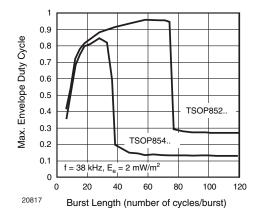


Fig. 7 - Max. Envelope Duty Cycle vs. Burst Length

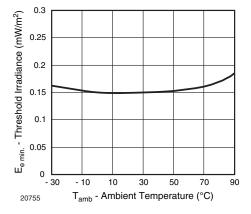
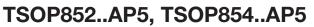


Fig. 8 - Sensitivity vs. Ambient Temperature





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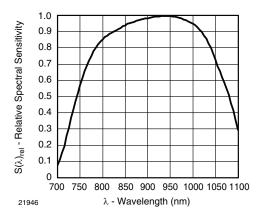


Fig. 9 - Relative Spectral Sensitivity vs. Wavelength

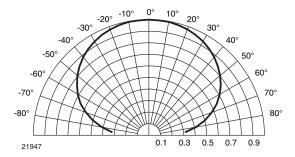


Fig. 10 - Directivity

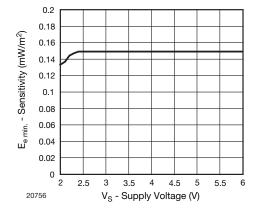


Fig. 11 - Sensitivity vs. Supply Voltage



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SUITABLE DATA FORMAT

The TSOP852..AP5, TSOP854..AP5 series is designed to suppress spurious output pulses due to noise or disturbance signals. Data and disturbance signals can be distinguished by the devices according to carrier frequency, burst length and envelope duty cycle. The data signal should be close to the band-pass center frequency (e.g. 38 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP852..AP5, TSOP854..AP5 in the presence of a disturbance signal, the sensitivity of the receiver is reduced to insure that no spurious pulses are present at the output. Some examples of disturbance signals which are suppressed are:

- DC light (e.g. from tungsten bulb or sunlight)
- · Continuous signals at any frequency
- Strongly or weakly modulated noise from fluorescent lamps with electronic ballasts (see figure 12 or figure 13)

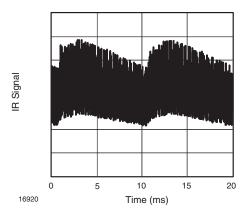


Fig. 12 - IR Signal from Fluorescent Lamp with Low Modulation

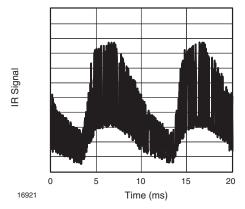


Fig. 13 - IR Signal from Fluorescent Lamp with High Modulation

	TSOP852AP5	TSOP854AP5
Minimum burst length	10 cycles/burst	10 cycles/burst
After each burst of length a minimum gap time is required of	10 to 70 cycles ≥ 10 cycles	10 to 35 cycles ≥ 10 cycles
For bursts greater than a minimum gap time in the data stream is needed of	70 cycles > 4 x burst length	35 cycles > 10 x burst length
Maximum number of continuous short bursts/second	1800	1500
Recommended for NEC code	Yes	Yes
Recommended for RC5/RC6 code	Yes	Yes
Recommended for Thomson 56 kHz code	Yes	Yes
Recommended for Mitsubishi code (38 kHz, preburst 8 ms, 16 bit)	Yes	No
Recommended for Sharp code	Yes	Yes
Suppression of interference from fluorescent lamps	Most common disturbance signals are suppressed	Even extreme disturbance signals are suppressed

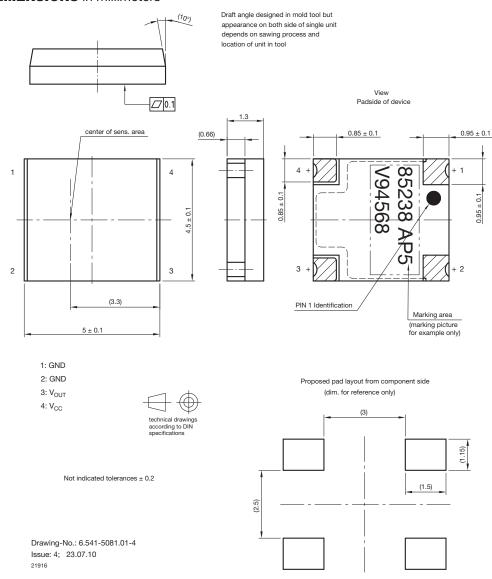
Notes

- For data formats with short bursts please see the datasheet for TSOP853...
- Example of compatible products for IR-codes:
 - -TSOP85436AP5: RC-5, RC-6, Panasonic
 - -TSOP85438AP5: NEC, Sharp, r-map
 - -TSOP85456AP5: r-step, Thomson RCA



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PACKAGE DIMENSIONS in millimeters



ASSEMBLY INSTRUCTIONS

Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Excercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

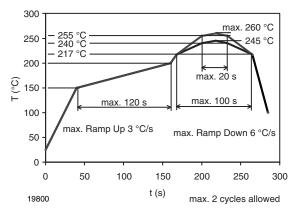
Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off

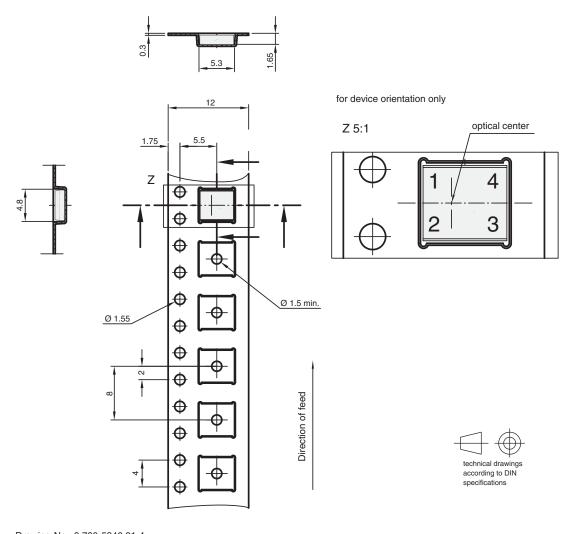


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VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



TAPING VERSION TSOP85..AP5 DIMENSIONS in millimeters



Drawing-No.: 9.700-5346.01-4 Issue: 2, 24.11.09

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LABEL

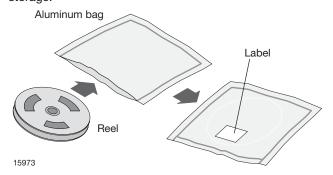
Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (finished goods)				
PLAIN WRITING	ABBREVIATION	LENGTH		
Item-description	-	18		
Item-number	INO	8		
Selection-code	SEL	3		
LOT-/serial-number	BATCH	10		
Data-code	COD	3 (YWW)		
Plant-code	PTC	2		
Quantity	QTY	8		
Accepted by	ACC	-		
Packed by	PCK	-		
Mixed code indicator	MIXED CODE	-		
Origin	xxxxxx+	Company logo		
Long bar code top	Туре	Length		
Item-number	N	8		
Plant-code	N	2		
Sequence-number	X	3		
Quantity	N	8		
Total length	-	21		
Short bar code bottom	Туре	Length		
Selection-code	X	3		
Data-code	N	3		
Batch-number	Х	10		
Filter	-	1		
Total length	-	17		

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition: 192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or

96 h at 60 °C + 5 °C and < 5 % RH for all device containers or

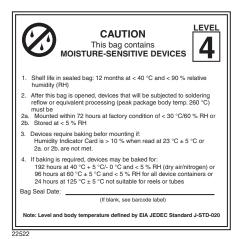
24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC $^{\! @}$ standard JSTD-020 level 4 label is included on all dry bags.



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EIA JETEC standard JSTD-020 level 4 label is included on all dry bags

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific

BAR CODE PRODUCT LABEL (example)





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Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.

Revision: 02-Oct-12 Document Number: 91000