

MCC

Micro Commercial Components
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2N7002V

Features

- Dual N-Channel MOSFET
- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed

Maximum Ratings @ 25°C Unless Otherwise Specified

Symbol	Rating	Rating	Unit
V_{DSS}	Drain-source Voltage	60	V
V_{DGR}	Drain-Gate Voltage	60	V
V_{GSS}	Gate-source Voltage	± 20	V
I_D	Drain Current	280	mA
P_D	Total Power Dissipation	150	mW
R_{JA}	Thermal Resistance Junction to Ambient	833	°C/W
T_J	Operating Junction Temperature	-55 to +150	°C
T_{STG}	Storage Temperature	-55 to +150	°C

Electrical Characteristics @ 25°C Unless Otherwise Specified

Symbol	Parameter	Min	Typ	Max	Units
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage*	60	70	---	Vdc
$V_{th(GS)}$	Gate-Threshold Voltage*	1.0	---	2.5	Vdc
I_{GSS}	Gate-body Leakage*	---	---	± 0.1	μ Adc
I_{DSS}	Zero Gate Voltage Drain Current*	---	---	1	μ Adc
	($V_{DS} = 60$ Vdc, $V_{GS} = 0$ Vdc)			500	
	($V_{DS} = 0$ Vdc, $V_{GS} = \pm 20$ Vdc, $T_j = 125$ °C)				
$I_{D(on)}$	On-state Drain Current*	0.5	1.0	---	Adc
	($V_{DS} = 7.5$ Vdc, $V_{GS} = 10$ Vdc)				
$r_{DS(on)}$	Drain-Source On-Resistance*	---	---	3.0	Ω
	($V_{GS} = 5$ Vdc, $I_D = 50$ mAdc)			2.0	
	($V_{GS} = 10$ Vdc, $I_D = 500$ mAdc)				
g_{FS}	Forward Tran Conductance*	80	---	---	ms
C_{iss}	Input Capacitance	---	---	50	pF
C_{oss}	Output Capacitance	---	---	25	
C_{rss}	Reverse Transfer Capacitance	---	---	5	
	$V_{DS} = 25$ Vdc, $V_{GS} = 0$ Vdc $f = 1$ MHz				

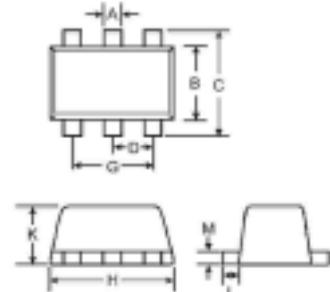
Switching

$t_{d(on)}$	Turn-on Time	$V_{DD} = 30$ Vdc, $V_{GEN} = 10$ Vdc	---	---	20	ns
$t_{d(off)}$	Turn-off Time	$R_L = 150\Omega$, $I_D = 200$ mA, $R_G = 25\Omega$	---	---	20	

* Pulse test, pulse width $\leq 300 \mu$ s, duty cycle $\leq 20\%$

N-Channel MOSFET

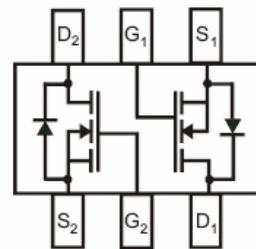
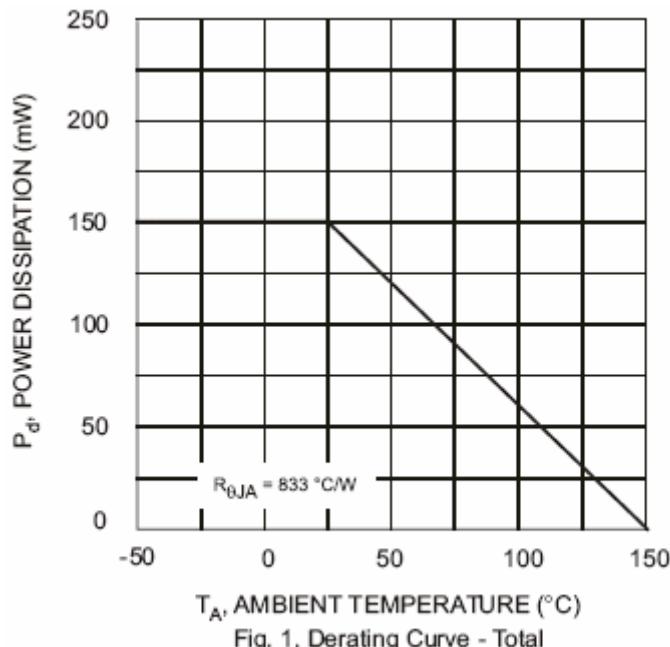
SOT-563



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	.006	.011	0.15	0.30	
B	.043	.049	1.10	1.25	
C	.061	.067	1.55	1.70	
D		.020		0.50	
G	.035	.043	0.90	1.10	
H	.059	.067	1.50	1.70	
K	.022	.023	0.56	0.60	
L	.004	.011	0.10	0.30	
M	.004	.007	0.10	0.18	

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Marking: KAS